

**METHOD AND CIRCUIT ARRANGEMENT FOR SYNCHRONIZATION OF  
SYNCHRONOUSLY OR ASYNCHRONOUSLY CLOCKED PROCESSOR UNITS**

Abstract

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A method implemented in hardware for synchronization of identical or different redundant processing units which process identical instruction sequences and are synchronously or asynchronously clocked. In the method, transactions that are  
10 external to the processing unit are used by modules assigned to the processing unit for synchronization of the processing unit in that the processing unit is delayed, in each case, by the assigned modules until the instruction execution of the processing units has reached the current transaction.